

Incremental Inductive Verification

slides adapted from the ones by

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Inductive Verification

In order to prove that $P(x)$ is \mathcal{M} -invariant, one possibility is to check if P is inductive. With 2 SAT-solver calls, we check the **validity** of:

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$$\text{(consecution)} \quad P \wedge T \Rightarrow P'$$

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Remember the discussion about k-induction we did on the 5th lecture?

The set of states in P could be much larger than the ones in I and contain states unreachable from I via T .

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 - $I \Rightarrow \phi_i$
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It follows that $P \wedge \bigwedge_{i=1}^{k-1} \phi_i$ is an inductive strengthening.

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- both methods do *not* compute a formula R for the exact set of reachable states in \mathcal{M} ;
- rather, they find a formula $F \wedge P$ that represents a larger set of states *all satisfying* $F \wedge P$:
 - \Rightarrow this F is a much smaller formula than R .

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- 3.1 if $err \wedge I$ is SAT, then stop: P is NOT invariant;

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At the end, the inductive strengthening (if any) will be:

$$P \wedge \bigwedge_{err \in CTI} \neg err$$

Incremental Proof - Example

Consider the program \mathcal{M}_1 :

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1      x, y := 1, 1
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Q: why?

We are not saying anything about x .

Incremental Proof - Example

We establish the first **inductive incremental** lemma $\phi_1 := x \geq 0$:

- $x = 1 \wedge y = 1 \Rightarrow x \geq 0$
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Now, $\phi_2 := y \geq 1$ is inductive **relative to** ϕ_1 :

- $x = 1 \wedge y = 1 \Rightarrow y \geq 1$
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We have found the inductive strengthening $\phi_1 \wedge \phi_2$, by means of an incremental proof.

Limitation of incremental proofs

Consider the program \mathcal{M}_2 :

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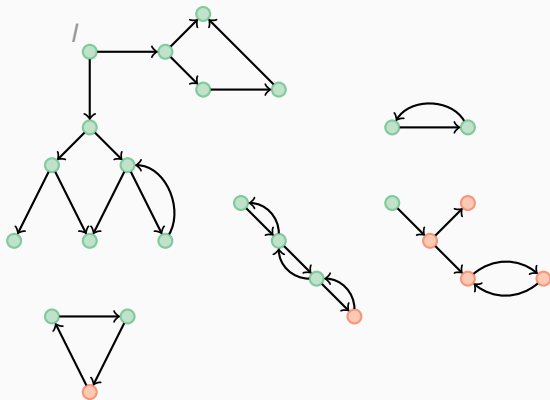
Monolithic approach = worst case of incremental proofs.

- **FSIS**: Finite-State Inductive Strengthening. It follows the incremental methodology [BM07].

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- “this algorithm is a result of asking the question: if the incremental method is often better for humans, might it be better for algorithms as well?” [Bra12];
- the core of the algorithm is the **generalization of an error state**.

FSIS - Example



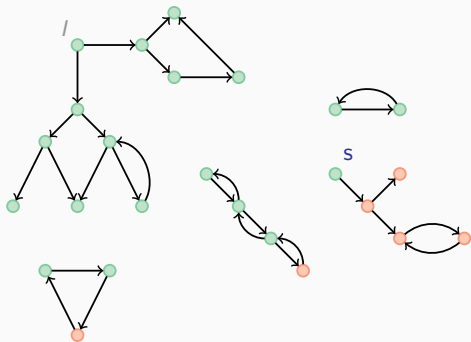
FSIS - Example - 1st iteration

Check if P is inductive (relative to nobody). Check the validity of:

$$\checkmark \quad I \Rightarrow P$$

$$\times \quad P \wedge T \Rightarrow P'$$

State s is a CTI.



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- s is a **cube** returned by the SAT-solver; $\neg s$ is a **clause** encoding all states different from s ;

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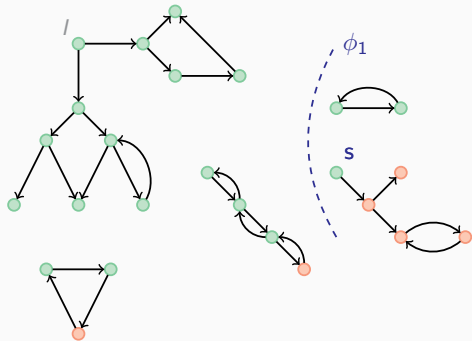
- s is a **cube** returned by the SAT-solver; $\neg s$ is a **clause** encoding all states different from s ;
- **generalization** of error state s : find a clause ϕ_1 such that
 - $\phi_1 \subseteq \neg s$; (*it excludes s*)
 - ϕ_1 is inductive (relative to its own); (*it includes at least all the reachable states*)
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 - ϕ_1 is minimal. (*it excludes the maximal number of non-reachable states*)
- if ϕ_1 does exist, it becomes the first **incremental lemma**.

FSIS - Example - 1st iteration

ϕ_1 can be thought as a "boolean" cutting plane.



Which states are excluded by ϕ_1 ? (i) those who can reach s
(ii) states "similar" to s (they share with s the dropped literals).

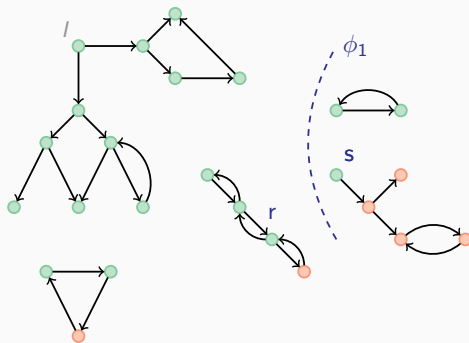
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Check if P is inductive **relative to** ϕ_1 :

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State r is a **CTI**.

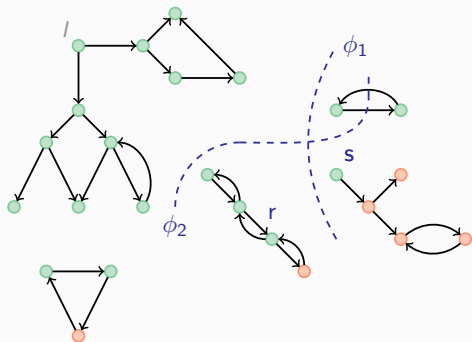


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- it would have been correct to generate ϕ_2 inductive (relative to its own), but it's more than what we need;
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- it would have been correct to generate ϕ_2 inductive (relative to its own), but it's more than what we need;
 - at the end we will consider the AND of all the lemmata;
- in general, it is faster to generate “inductive relative to” clauses.
 - intuitively, we are considering many fewer states of the system.

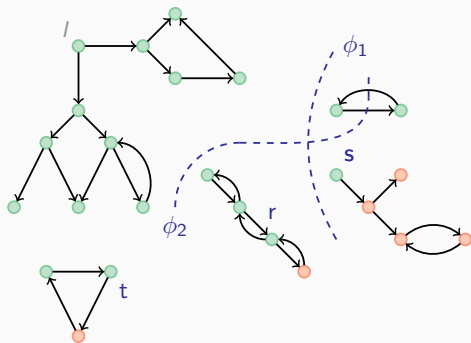
FSIS - Example - 3rd iteration

Check if P is inductive **relative to** $\phi_1 \wedge \phi_2$:

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State t is a **CTI**.

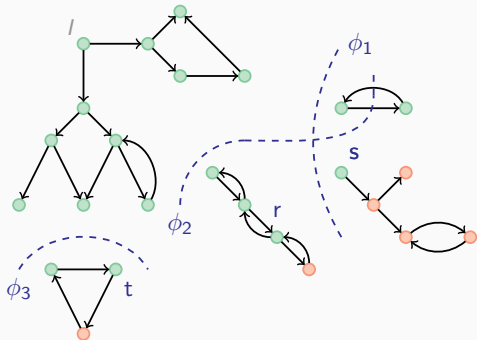


FSIS - Example - 3rd iteration

- generalization of error state t :
 - $\phi_3 \subseteq \neg t$;
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- $\phi_1 \wedge \phi_2 \wedge \phi_3 \wedge P$ is an inductive strengthening.
- P is \mathcal{M} -invariant.

- suppose that an error state s does *not* have a minimal inductive generalization;

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- **worst case**: we proceed with the monolithic technique;

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- eventually,
 - either $I \wedge \neg P$ is SAT: P is not invariant;
 - or we find an inductive strengthening $P \wedge \bigwedge_{i=0}^n \phi_i$;

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Parallelization:

- straightforward; "by simply using a randomized decision procedure to obtain the CTIs, each process is likely to analyze a different part of the state-space." [BM07]

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- IC3 de-emphasized global information in favor of stepwise information: we will generate clauses that ensure that an error is unreachable up to some number of steps.

Sequence of frames $F_0(= I), F_1, F_2, \dots, F_k$:

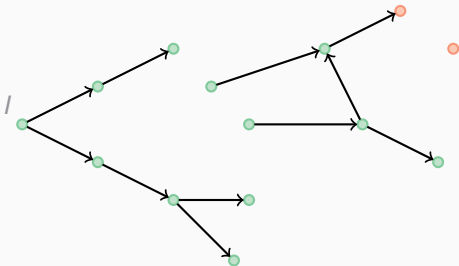
- each F_i is an over-approximation of the set of states reachable in at most k steps;
- each F_i is a set of clauses, *i.e.*, a CNF formula;
- the algorithm stops when $F_i \equiv F_{i+1}$. We will maintain the invariant that $clauses(F_{i+1}) \subseteq clauses(F_i)$: the equivalence check is simply a syntactic test: $F_i = F_{i+1}$.

IC3 - 1st iteration

Check if there are counterexamples of length 0 or 1 with these two SAT-queries:

$$\times I \wedge \neg P$$

$$\times F_0(= I) \wedge T \wedge \neg P'$$



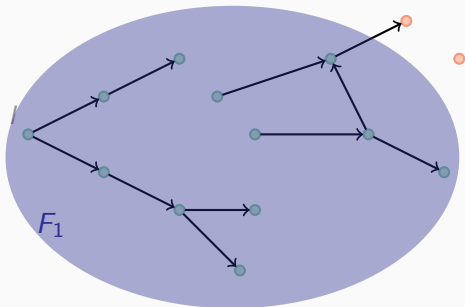
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Since $F_0 \wedge T \Rightarrow P'$, we set $F_1 := P$. (over-approximation)



IC3 - 2nd iteration

At iteration k , check if $F_k \wedge T \wedge \neg P'$; in this case ($k = 1$):

$$\checkmark \quad F_1 \wedge T \wedge \neg P'$$

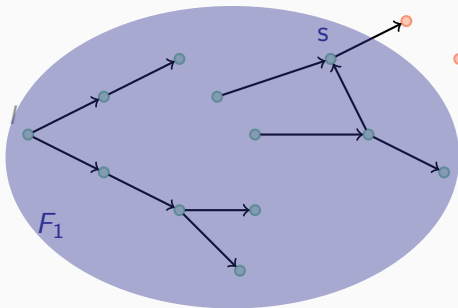
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$\neg s$ is inductive relative to $F_0(= I)$: error state s is **not** reachable in at least $k = 1$ step. We find a minimal $\phi_1 \subseteq \neg s$ such that ϕ_1 is inductive **relative to** $F_0(= I)$.

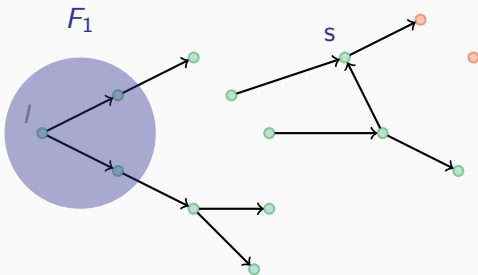
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$\Rightarrow \phi_1$ excludes the error state s (and similar states) but contains at least all the states reachable in at most $k = 1$ steps.

We add ϕ_1 to all the previous frames. In this case $F_1 := F_0 \wedge \phi_1$.



IC3 - Blocking phase

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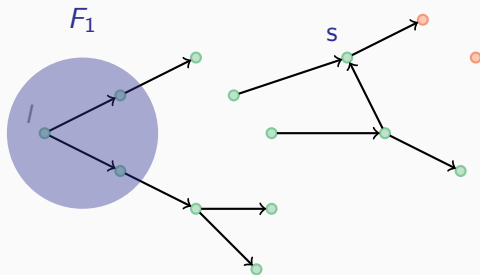
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if $\neg s$ is inductive relative to F_{k-1} , then generate a minimal subclause $c \subseteq \neg s$ inductive relative to F_{k-1} , *i.e.*, **c holds for at least all states reachable in i steps.**

\Rightarrow add c to frames $F_0 \dots F_{k+1}$, *i.e.*, refine the over-approximations.

IC3 - 2nd iteration

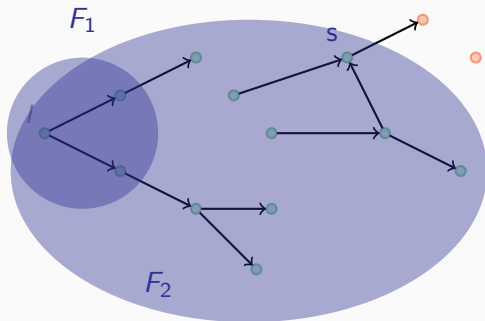
We create a new frame only when $F_k \wedge T \Rightarrow P'$ is valid.



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In this case, $F_1 \wedge T \Rightarrow P'$ is valid. We create a new frame $F_2 := P$.



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For all $0 \leq i \leq k$ and $c \in F_i$, check if

$$F_i \wedge T \Rightarrow c'$$

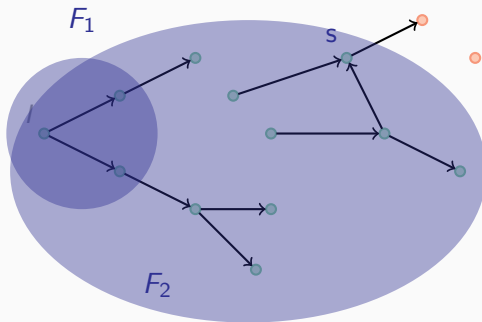
If $c \notin \text{clauses}(F_{i+1})$, then set $F_{i+1} := F_{i+1} \cup \{c\}$

\Rightarrow it propagates forward the errors

\Rightarrow it helps the discovery of mutually inductive clauses

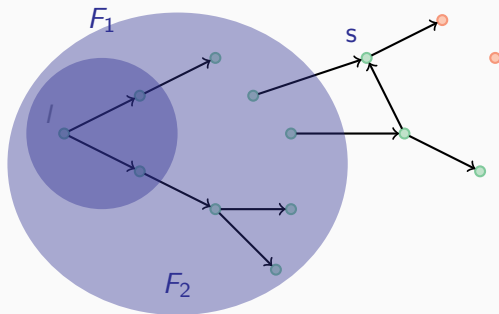
IC3 - 3rd iteration

Check if $F_2 \wedge T \wedge \neg P'$ (\checkmark). $\neg s$ is inductive relative to F_1 : error state s is **not** reachable for at least $k = 2$ steps.



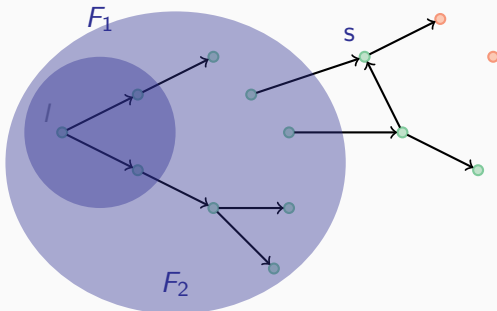
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Check if $F_2 \wedge T \wedge \neg P'$ (\checkmark). $\neg s$ is inductive relative to F_1 : error state s is **not** reachable for at least $k = 2$ steps. **Blocking phase**: find minimal subclause $\phi_2 \subseteq \neg s$ inductive relative to F_1 . Add ϕ_2 to frames F_0 and F_1 .



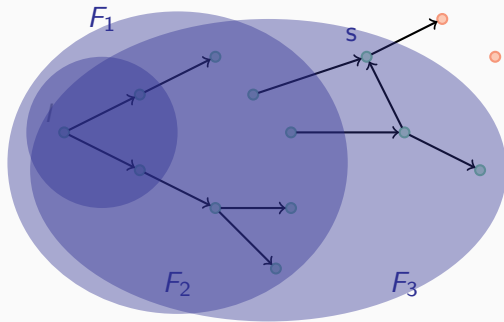
IC3 - 4rd iteration

Since $F_2 \wedge T \Rightarrow P'$ is valid, we create a new frame $F_3 := P$.



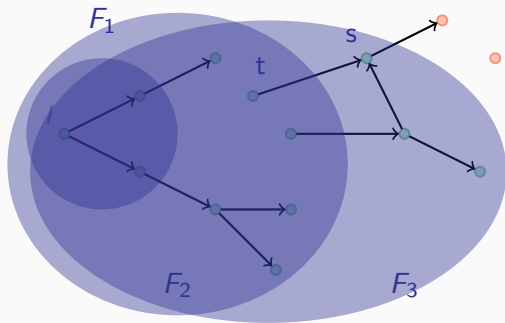
IC3 - 4rd iteration

Since $F_2 \wedge T \Rightarrow P'$ is valid, we create a new frame $F_3 := P$.



IC3 - 4rd iteration

Again $F_3 \wedge T \wedge \neg P'$ (\checkmark). But now $\neg s$ is **not** inductive relative to F_2 : error state s could be reachable in $k = 3$ steps ...



Instead of generating a clause that excludes s (it is possible), we call the algorithm **recursively** on the predecessor t of s

... remember that t could still be reachable as far as we know ...

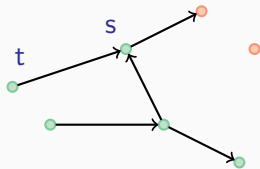
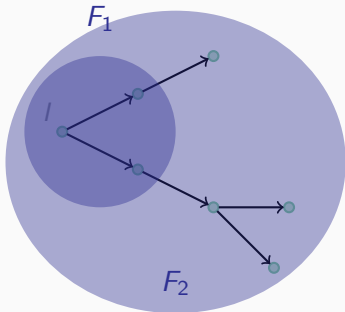
Instead of generating a clause that excludes s (it is possible), we call the algorithm **recursively** on the predecessor t of s

... remember that t could still be reachable as far as we know ...

" t is the **new** s " ;-)

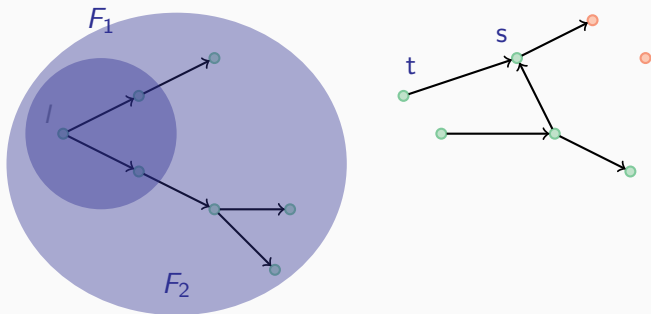
IC3 - Recursion

We want to remove error state t from F_2 . $\neg t$ is inductive relative to F_1 : find min subclause $\phi_4 \subseteq \neg t$ and add it to F_0, F_1, F_2 .



IC3 - Recursion

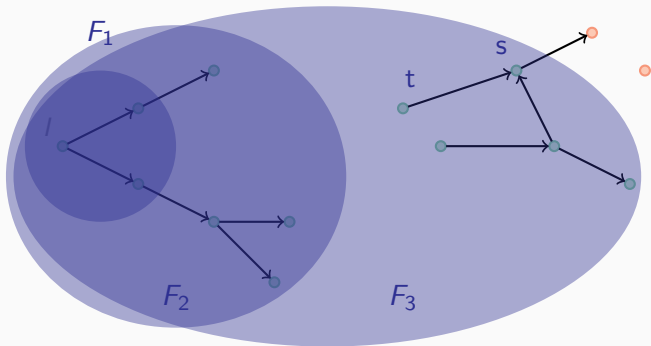
We want to remove error state t from F_2 . $\neg t$ is inductive relative to F_1 : find min subclause $\phi_4 \subseteq \neg t$ and add it to F_0, F_1, F_2 .



If in this process we had gone back with recursion until an initial state, then we would have found a **counterexample**.

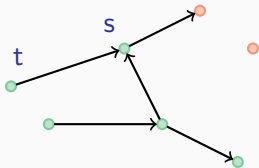
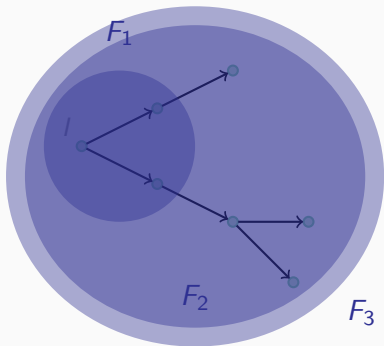
IC3 - Termination

Now error state s in frame F_3 can be generalized: find min clause $\phi_5 \subseteq \neg s$ inductive relative to F_2 .



IC3 - Termination

Now error state s in frame F_3 can be generalized: find min clause $\phi_5 \subseteq \neg s$ inductive relative to F_2 .



$F_2 = F_3$: IC3 terminates with **True**.

- FAIR: IC3 for ω -regular properties (e.g., LTL) [Bra+11];
- IICTL: IC3 for CTL properties [HBS12];
- Infinite-state: software model checking via IC3 [CG12].

- `go_msat` : initialise the system for infinite state model checking using the SMT solver `mathsat5`.
- `check_invar_ic3` : check invariant using IC3-based algorithm.
- `check_ltlspec_ic3` : check LTL using IC3-based algorithm.

But IC3 is for reachability, how do we apply it for checking LTL properties?

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