

Formal methods 2018-2019

Lab sessions info

Enrico Magnago

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- Enrico Magnago.
- First year PhD student at the Embedded Systems unit in FBK.
- PhD topic: model checking of timed systems (e.g. real-time systems).
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Objectives

- Write formal models in Promela and SMV languages.
- Inspect possible executions of a models.
- Write and verify properties.

Model checkers

1. SPIN
2. NUSMV- NUXMV

Resources

- Slides, examples and solutions: enricomagnago.com
- Previous years (including exams): [Patrick Trentin website](#)

Schedule

- Friday 9:30-11 am, A202

Exam will ask you to:

- write formal models in Promela and SMV languages;
- simulate these models;
- verify some properties on them.

User manuals of tools available during the exam.

Remember: previous years exams are [available](#).